

REMARKS

This Response is submitted in response to the Notice of Non-Compliant Amendment dated August 19, 2004. The Notice objected to the amendments to the drawings submitted in the Response to Office Action dated July 28, 2004. The Notice requires submission of only the corrected section of the allegedly non-compliant amendment, that is, the Amendments to the Drawings section.

Applicants submit the amendments to the drawings submitted with the July 28 Response were proper. However, this Response includes a revised Amendments to the Drawings section of the July 28 Response.

Applicants invite the Examiner to call Applicants' Representative to discuss any issues with this application.

Drawings and Specification

On pages 2-4 of the Office Action dated January 28, 2004, the drawings and Specification were objected to for informalities.

As to the drawing objections, the Office Action states that the previous proposed drawing corrections were disapproved. Accordingly, the amendments to the drawings in this Response are based on the drawings originally filed with the application. Originally filed Figs. 1-5, 8-10 and 20 have been canceled without prejudice. Originally filed Figs. 6, 7 and 11-19 have been amended to renumber the figures in view of the canceled figures. Figs. 6, 7 and 11-19 have been renumbered as Figs. 1-11, respectively.

As to the specification objections a substitute specification was required before the application passes to issue. A substitute specification will be prepared to correct the informalities upon approval of the drawing corrections. The substitute specification will be submitted prior to issuance of the application.


CONCLUSION

For the foregoing reasons, Applicants submit that the patent application is in condition for allowance and request a Notice of Allowance be issued.

The Commissioner is authorized to charge and credit Deposit Account No. 02-1818 for any fees associated with the submission of this Response to Office Action.

Respectfully submitted,

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AMENDMENTS TO THE DRAWINGS

Please delete Figs. 1-5, 8-10 and 20 originally filed with the application. Please replace originally filed Figs. 6, 7 and 11-19 with the enclosed four Replacement Sheets of drawings renumbered as Figs. 1-11, respectively.

Annotated marked-up drawings of originally filed Figures 1-29 are also enclosed which show annotated revisions which result in the enclosed Replacement Sheets of Figs. 1-11.



FIG. 5

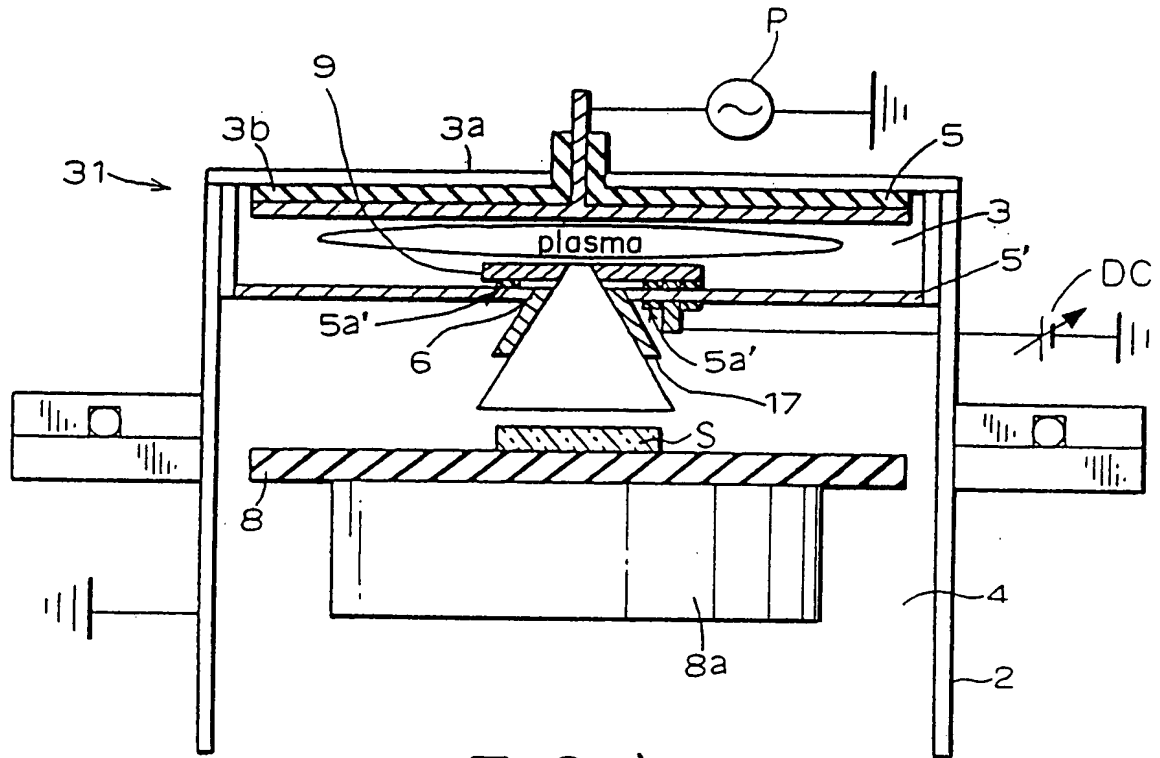


FIG. 6

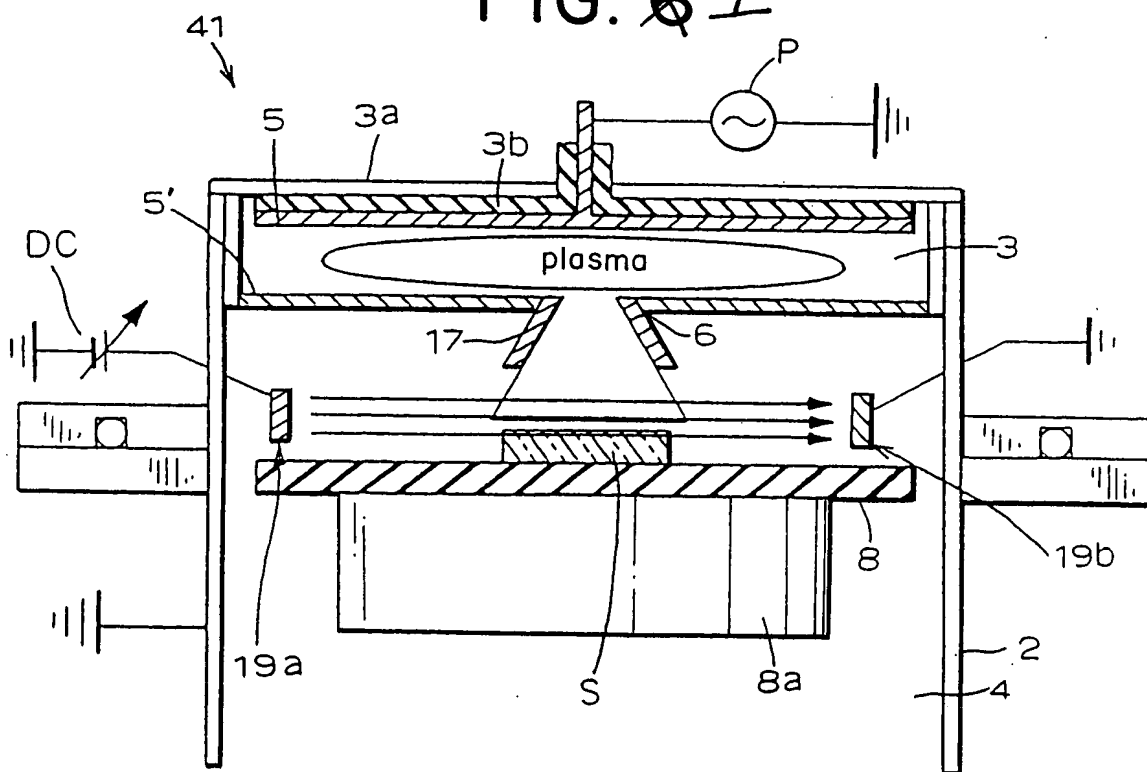




FIG. ~~7~~ 2

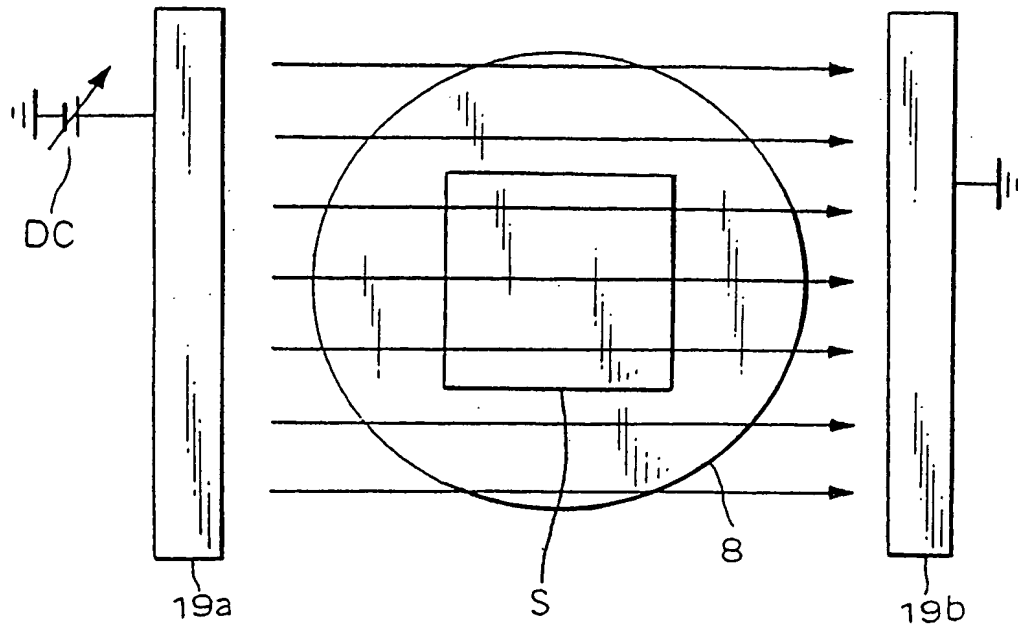


FIG. 8

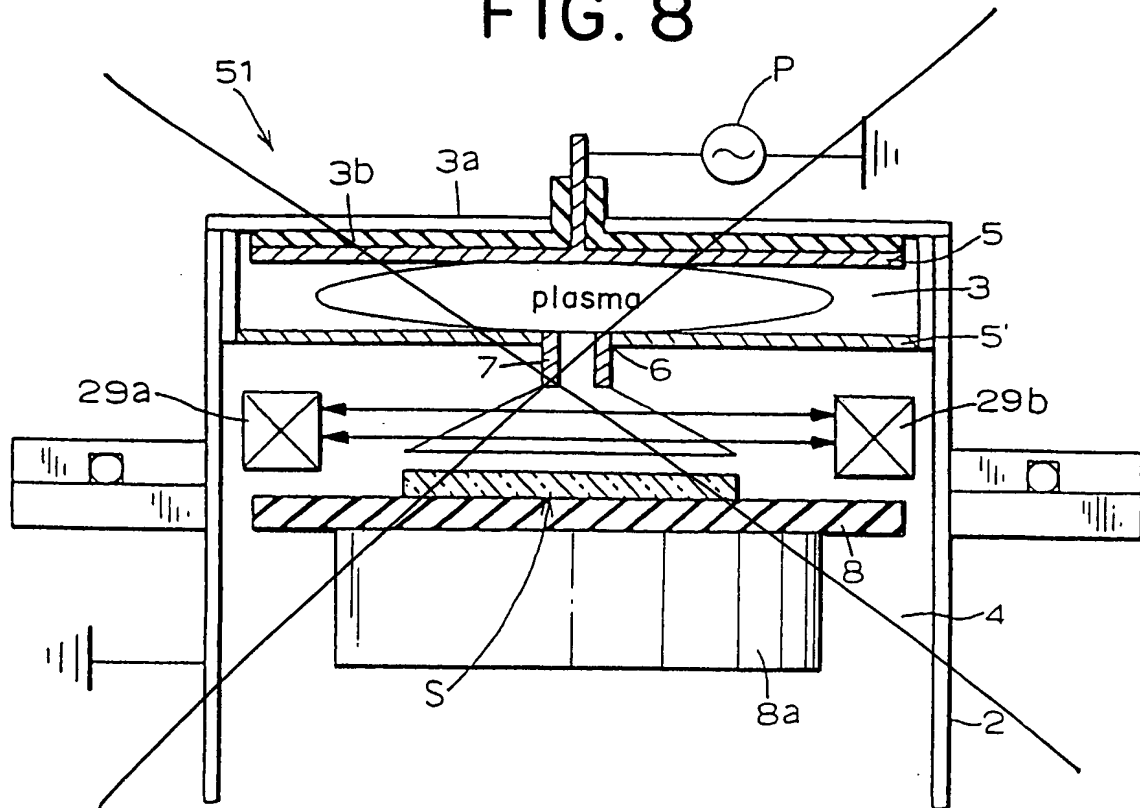




FIG. 9

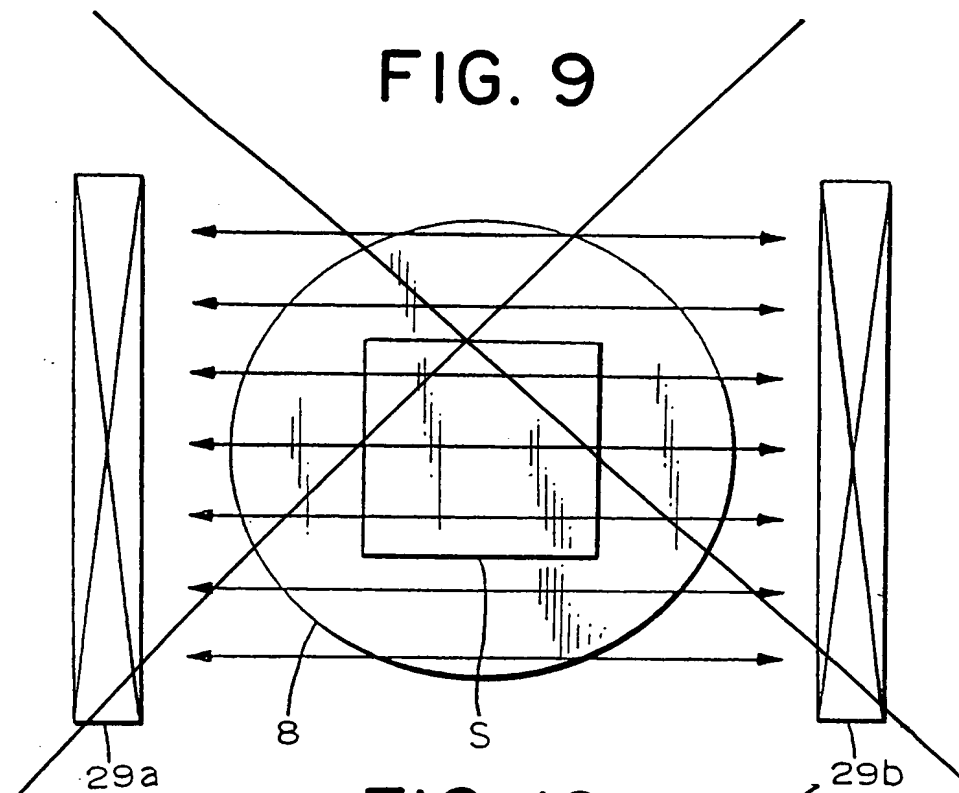


FIG. 10

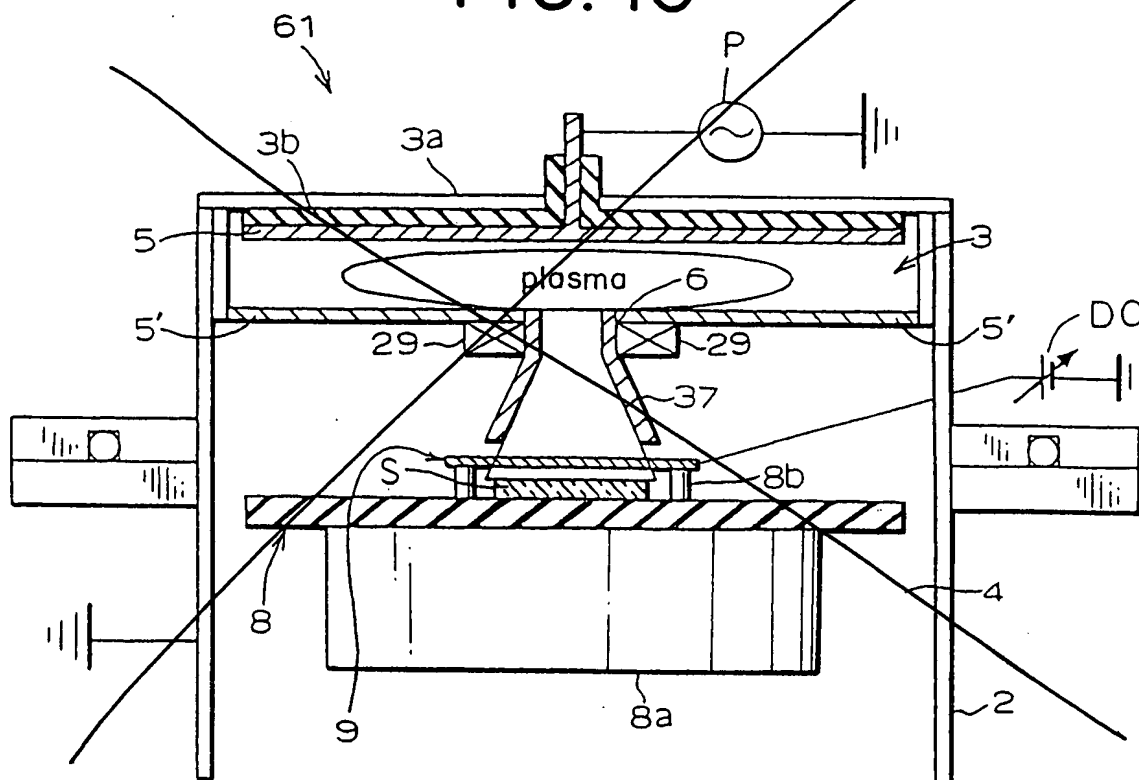




FIG. ~~1~~ 3

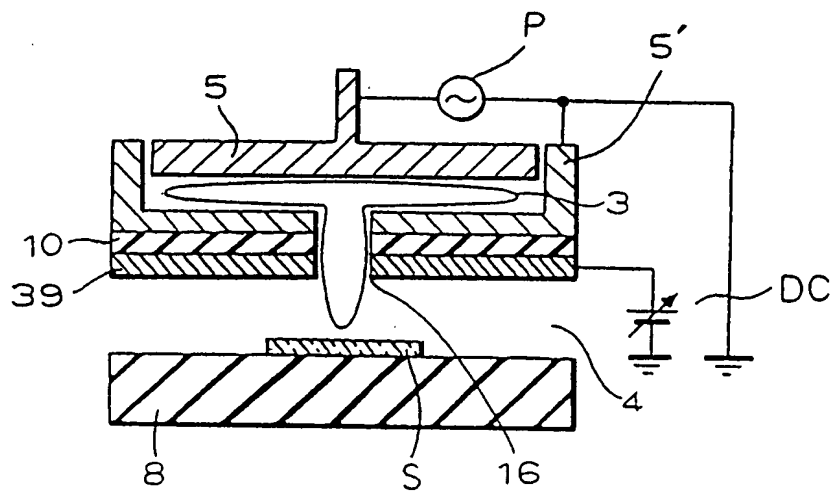


FIG. ~~12~~ 4

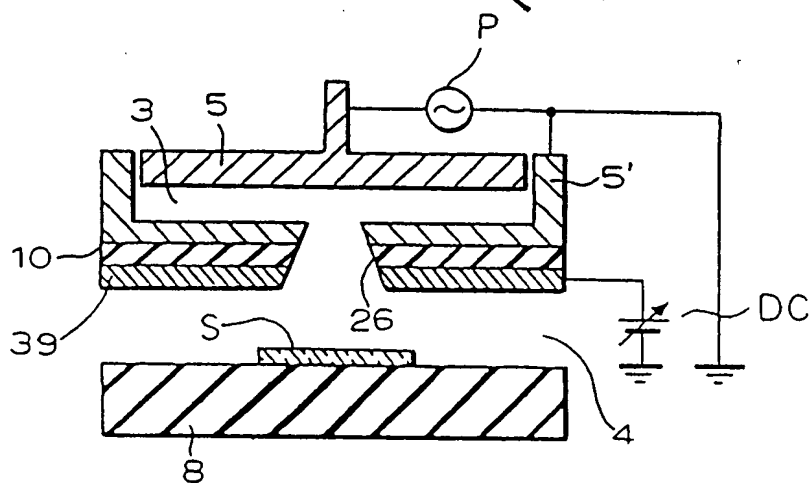


FIG. ~~13~~ 5

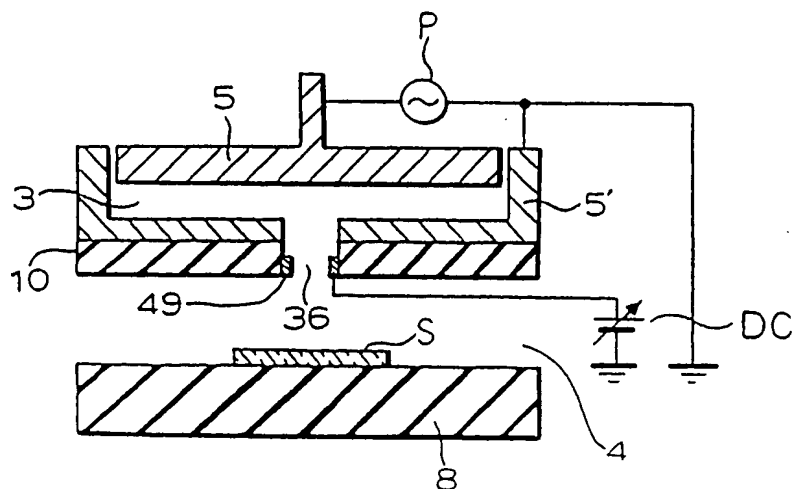




FIG. ~~14~~ 6

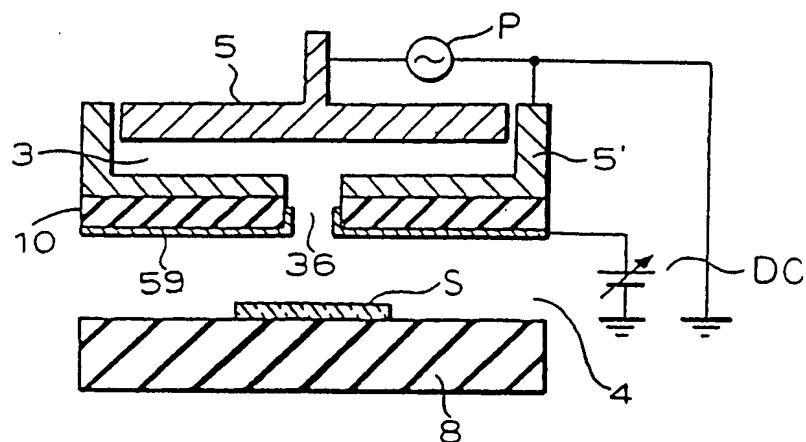


FIG. ~~15~~ 7

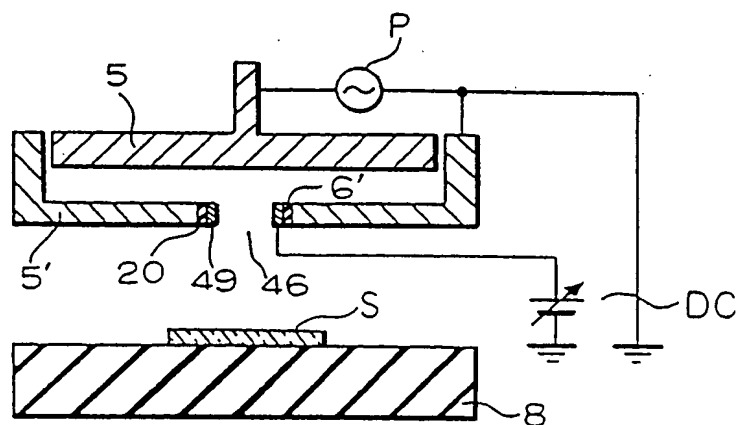


FIG. ~~16~~ 8

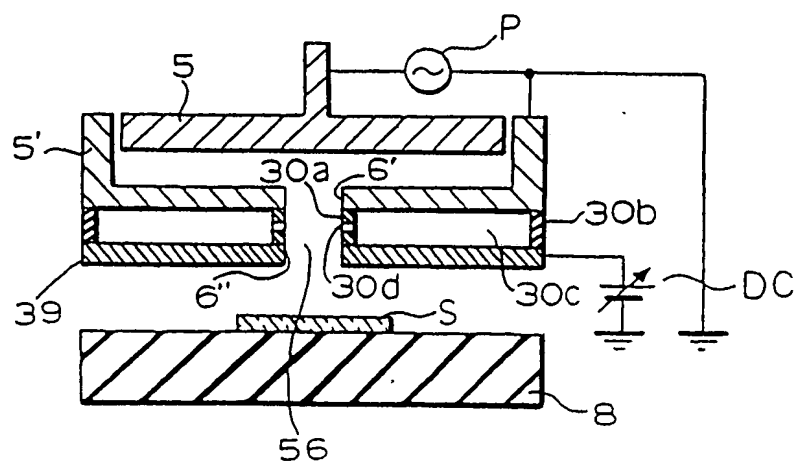


FIG. 1 is a perspective view of a first embodiment of a semiconductor device. The device includes a substrate 39 with a top layer 10. On top of layer 10 are several rectangular regions 5' separated by vertical dividers 66. A DC voltage source is connected to the bottom of the substrate 39.

FIG. 1 is a perspective view of a first embodiment of a device. The device consists of a substrate 39 and a series of parallel strips 10. Each strip 10 has a top layer 5' and a bottom layer 76. A DC voltage source is connected to the bottom layer 76 of the strips.

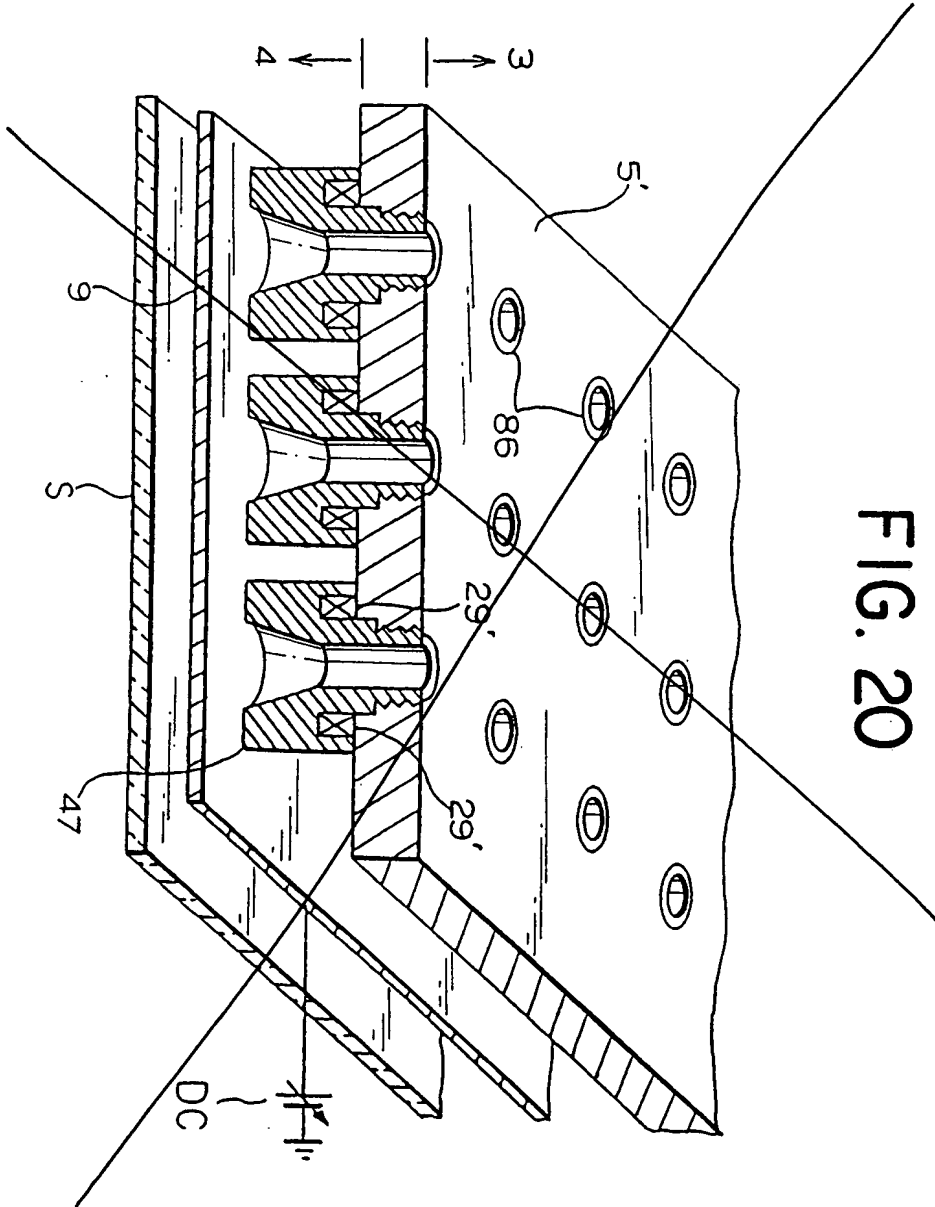


FIG. 20